

### **AMENDMENTS TO THE SPECIFICATION**

*Please amend paragraph [0032] of the Specification as follows:*

[0032] The relationship between a control signal of the selector/arithmetic unit k and a selector/arithmetic unit output is shown in FIG. 14 in a table format. An output depends on the importance of components and their positions in the registers of the important component selection apparatus. The dependence is represented by combinational logic functions of comparator outputs. The symbol \* indicates unknown or don't care as a logic value.  $K_s$  indicates the index number of a ~~resister~~register that stores the same component identifier as input data  $D_{in}$ . That is,  $S(K_s)=1$ . When there is no data in the registers that has the same component identifier as input data,  $S(k)=0$  for every k and  $LS(n)=0$  and thus for convenience it may be regarded such that  $K_s=n+1$ .

[0032] The relationship between a control signal of the selector/arithmetic unit k and a selector/arithmetic unit output is shown in FIG. 14 in a table format. An output depends on the importance of components and their positions in the registers of the important component selection apparatus. The dependence is represented by combinational logic functions of comparator outputs. The symbol \* indicates unknown or don't care as a logic value.  $K_s$  indicates the index number of a register that stores the same component identifier as input data  $D_{in}$ . That is,  $S(K_s)=1$ . When there is no data in the registers that has the same component identifier as input data,  $S(k)=0$  for every k and  $LS(n)=0$  and thus for convenience it may be regarded such that  $K_s=n+1$ .